

KESHAV MAHAVIDYALAYA
(University of Delhi)

Faculty Time Table

Faculty Name: Mr. Anil Sethi	Department: Electronics	Session: 2026-27 (Odd Sem)
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Period	I	II	III	IV	1:00 PM	V	VI	VII	VIII
Days/Time	9:00 AM	10:00 AM	11.00 AM	12.00 Noon		1:30 PM	2:30 PM	3:30 PM	4:30 PM
Monday		B.Sc(H) Electronics V Embedded Systems Practicals (#306)	B.Sc(H) Electronics I Circuit Theory and Network Analysis (#304)	B.Sc(H) Electronics I Circuit Theory and Network Analysis (#304)	L U N C H B R E A K	B.Sc(H) Electronics V Basic VLSI Design (AS)(#307)			
Tuesday			B.Sc(H) Electronics III Analog Electronics-II Practicals (#306)			B.Sc(H) Electronics I Circuit Theory and Network Analysis (#304)			
Wednesday		B.Sc(H) Electronics III Engineering Mathematics Practicals (#303)	B.Sc(H) Electronics I Programming Fundamentals using Python Practicals (#306)						
Thursday			B.Sc(H) Electronics V Basic VLSI Design Practicals (#303)			B.Sc(H) Electronics V Basic VLSI Design (AS)(#307)			
Friday			B.Sc(H) Electronics I Circuit Theory and Network Analysis Practicals (#303)			B.Sc(H) Electronics V Basic VLSI Design (AS)(#307)			

Details of Workload

S. No.	Course & Semester	Paper Name (with Paper Code)	Lect./Prac./Tut.	Number of Periods/Week
1	B.Sc(H) Electronics I	Circuit Theory and Network Analysis (2512011102)	3/2/0	5
2	B.Sc(H) Electronics III	Analog Electronics-I I(2512012302)	0/2/0	2
3	B.Sc(H) Electronics I	Programming Fundamentals using Python (2512011101)	0/2/0	2
4	B.Sc(H) Electronics V	Embedded Systems (2512013501)	0/1/0	1
5	B.Sc(H) Electronics III	Engineering Mathematics (2512012301)	0/1/0	1
6	B.Sc(H) Electronics V	Basic VLSI Design (2512013503)	3/2/0	5

	Teacher In-Charge/Course Coordinator	Convener/Member Central Time-Table Committee	Principal